

Figure 1 - Digitizer with AGC

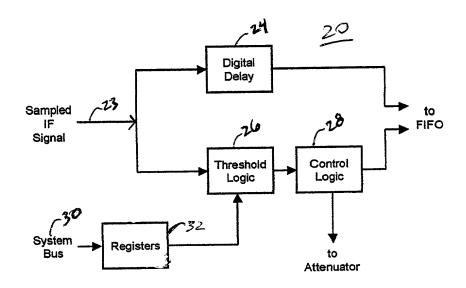


Figure 2 - Signal Detection Logic

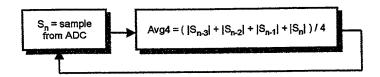


Figure 3a - Sampling Logic

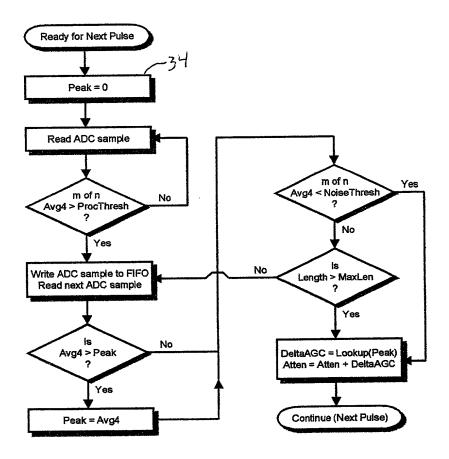


Figure 3b - AGC Logic

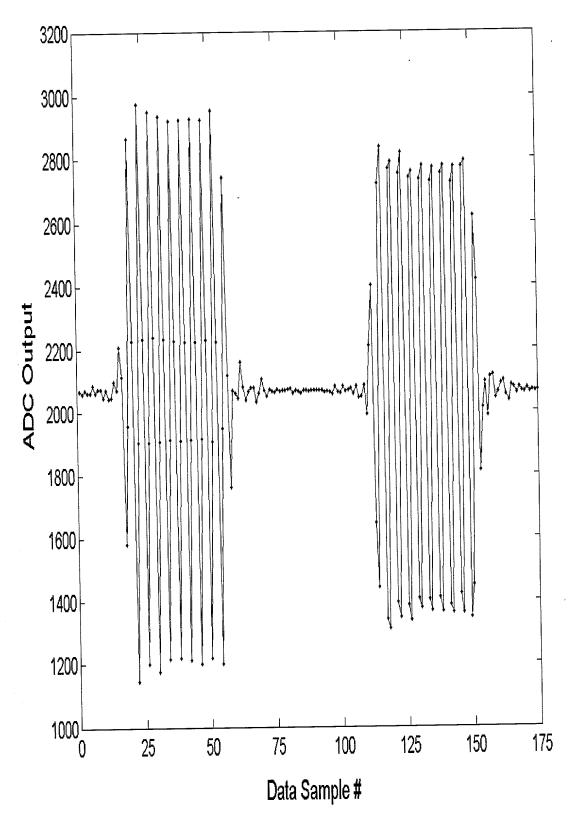


Figure 4

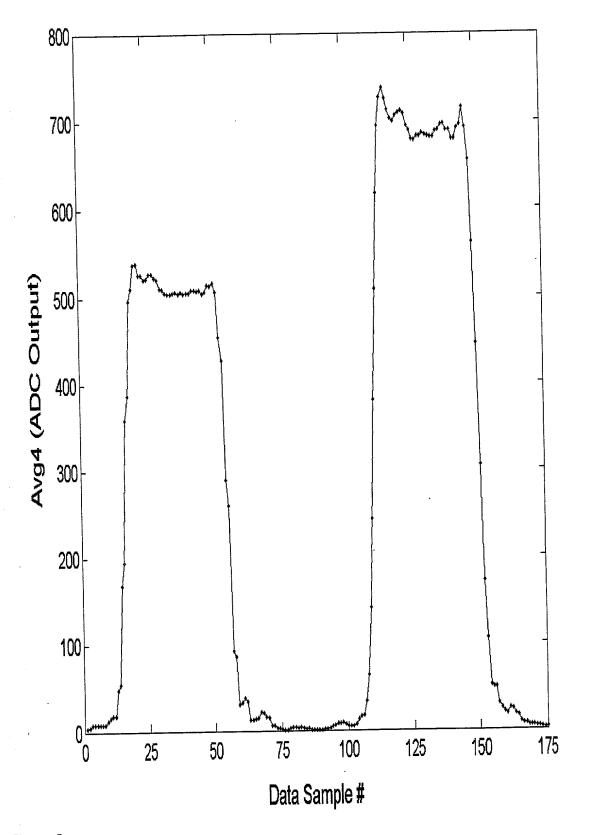


Figure 5